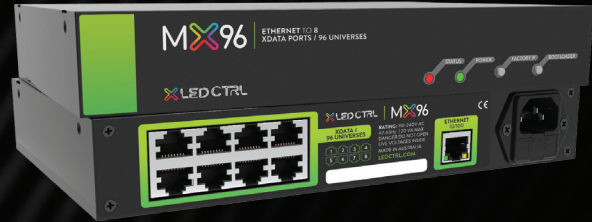




MX96

96 UNIVERSE / 8-PORT
ETHERNET TO SPI CONTROL
SYSTEM (artnet/sACN)



INTRO

The MX96 Master coupled with SX Slaves, is a powerful and flexible control system developed specifically to control LED pixel protocols in a wide range of installation configurations.

The MX96 outputs the transmission of light data ("X-Data") to be sent through any standard network cable at distances in excess of 300m (1000'). The SX Slaves decode this signal into pixel ready protocols and allows the distribution of a large number of pixels from a single, centralised pixel controller, bypassing common challenges of data degradation, crosstalk, signal reflections or voltage drop.

A whopping 96 Universes of Multicast/Unicast E1.31 or ART-NET data is transmitted via 8 outputs.

This incredible pixel volume when combined with our advanced and highly configurable feature-set ensures our MX96 and SX pixel control system is the perfect choice for your LED Pixel lighting projects.

CONFIGURATION OPTIONS

OPERATING SPECIFICATIONS:

- Input Power: 110-240v AC
- Output Protections:
 - Electrostatic Discharge (ESD)
 - Overvoltage protection (up to 60vDC)
 - Short circuit
- Input Connection: Ethernet (RJ45)
- Input Protocol: E1.31(sACN) / Artnet
- Output Connection:
 - 8x Ethernet (RJ45)
- Output Protocol:
 - 8x X-Data
- Operating Temp:
 - -40°C - +80°C
- Storage Temp:
 - -50°C - +150°C
- Supported Chipsets:
 - TLS3001, SM16716, LPD6803, WS2801, WS2811/12/12B/13/18, APA102/104, TM180x, MBI6020, INK1003, SK6812, UCS1903, UCS2903, UCS2904, MY9221, MY9231

(NB: One protocol per MX96)

OPERATING MODES:

There are two operating modes, condensed and expanded for greater adaptability and allowing optimised configuration of pixels for different project requirements.

1. CONDENSED

Each SX receiver has two pixel data outputs. A maximum of 3,072 channels per output is available in this mode. (6,144 channels total).

This mode allows control of all pixel protocols, either with or without a clock line.

2. EXPANDED

Each SX receiver has up to four pixel data outputs. A maximum of 1,536 channels per output. (6,144 channels total).

This mode can only be used for pixel protocols without a clock line.



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